	Application No.	Applicant(s)
Notice of Allowability	10/660,632	SUMITA, MASAYA
	Examiner	Art Unit
	David Lam	2818
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313  1.  This communication is responsive to pre-amendment file or	(OR REMAINS) CLOSED ir or other appropriate commu GHTS. This application is s and MPEP 1308.	this application. If not included unication will be mailed in due course. <b>THIS</b>
	<u>19/12/03</u> .	
2. X The allowed claim(s) is/are <u>1-25</u> .		
3. The drawings filed on are accepted by the Examiner		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority un</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority documents have</li> <li>International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>	been received. been received in Application	n No
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminification (PTO-152) which give		
6. X CORRECTED DRAWINGS ( as "replacement sheets") mus	t be submitted.	
(a) including changes required by the Notice of Draftspers		v ( PTO-948) attached
1)  hereto or 2)  to Paper No./Mail Date		
(b) ⊠ including changes required by the attached Examiner's Paper No./Mail Date _(o)0. ↓ Identifying indicia such as the application number (see 37 CFR 1.		
each sheet. Replacement sheet(s) should be labeled as such in the		
<ol> <li>DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT F</li> </ol>		
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🗌 Interview Si	formal Patent Application (PTO-152) ummary (PTO-413),
<ul> <li>3.  Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 9/12/03</li> <li>4.  Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	8), 7. ⊠ Examiner's	_
·		DAVID LAM

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04) PRIMARY EXAMINER

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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

## IN THE DRAWING:

- Figures 27-29 have been labeled as "Prior Art". See attachment.

## Allowable Subject Matter

2. The following is an examiner's statement of reasons for allowance: Claims 1-25 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: a memory device comprising at least one read/write control signal line, among others as claimed in independent claim 1, for transmitting a read/write control signal to a memory cell, wherein the write control signal and read control signal are controlled so as not to be activated at the same time; a reset signal, among others as claimed in independent claim 21, activated to set a state of storage element to have a desirable value for a period other than the reading and writing operation; a NOT signal of AND of the write control signal and inverted write signal, among others as claimed in independent claim 24, connected to a first source of a transistor constituting a second inverting logic gate, and an AND signal of the write control

signal and the write signal is connected to a second source of the transistor constituting the second inverting logic gate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- Sawada (6,014,340) disclose a synchronous semiconductor memory device having internal circuitry enabled only when commands are in normal sequence.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

October 27, 2004

DAVID LAM

PRIMARY EXAMPLER